A picture containing text, electronics, computer

Description automatically generated

Continue is triggered to execute the SORT FUNCTION

Switches are set to 10’b 00 0101 1010

(16’h005A) to jump to the start address x005A of the Sort Test program

Set SW to 10’b 00 0000 0010 (16’h0002) to call the SORT FUNCTION

Reset the entire SLC-3 processor

All registers are cleared

FSM is set to Halted state

Switches are first set to 10’b 00 0000 0000

Original Data stored in the memory from address 74 to 89 before SORT FUNCTION executes

Sorted Data stored in the memory from address 74 to 89 after SORT FUNCTION finishes execution